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FORM PTO-1449	SERIAL NO. 09/748,589	CASE NO. 10519/9
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE December 22, 2000	GROUP ART UNIT 2187
(use several sheets if necessary)	APPLICANT(S): MARCH ET AL.	

EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)	
GJP	A21	"Method for Deleting Stored Digital Data from Write-Once Memory Device," U.S. Patent Application Serial No. 09/638,439, filed August 14, 2000; inventors: Christopher S. Moore, Derek J. Bosch, Daniel C. Steere, and J. James Tringali.
GJP	A23	"Three-Dimensional Memory Array and Method of Fabrication," U.S. Patent Application Serial No. 09/560,626, filed April 28, 2000; inventor: Johan Knall.
GJP	A24	"Write-Once Memory Array Controller, System, and Method," U.S. Patent Application Serial No. 09/638,427, filed August 14, 2000; inventors: Derek J. Bosch, Christopher S. Moore, Daniel C. Steere, and J. James Tringali.
GJP	A25	"Low-Cost Three-Dimensional Memory Array," U.S. Patent Application Serial No. 09/638,428, filed August 14, 2000; inventors: Mark G. Johnson, Thomas H. Lee, Vivek Subramanian, and P. Michael Farmwald.
GJP	A26	"Modular Memory Device," U.S. Patent Application Serial No. 09/638,334, filed August 14, 2000; inventors: J. James Tringali, P. Michael Farmwald, Thomas H. Lee, Mark G. Johnson, and Derek J. Bosch.

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EXAMINER GARY J. PORTKA	DATE CONSIDERED 8/29/02
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.